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## 31: Direct metallisation method onto 3-D printed polyetherimide substrates

Dr Thomas D.A. Jones<sup>1</sup>, Ms Assel Ryspayeva<sup>1</sup>, Dr Mohammadreza N. Esfahani<sup>2</sup>, Professor Russel Harris<sup>2</sup>, Dr. Robert W. Kay<sup>2</sup>, Dr Jose Marques-Hueso<sup>1</sup> and Professor Marc P.Y. Desmulliez<sup>1</sup>

<sup>1</sup>Heriot-Watt University, School of Engineering & Physical Sciences, Nature Inspired Manufacturing Centre, Edinburgh EH14 4AS, Scotland, UK

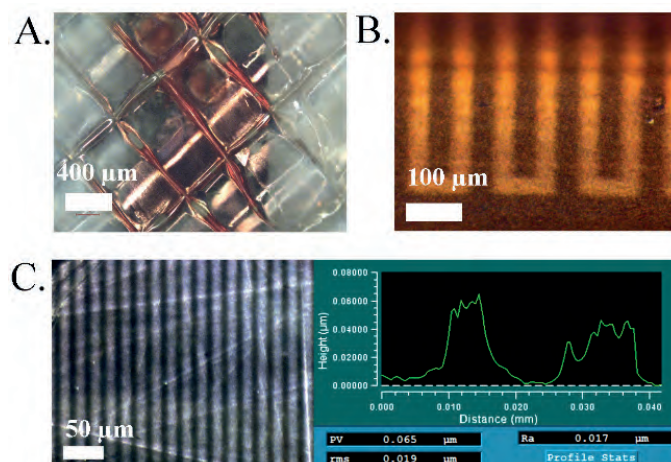
<sup>2</sup>Institute of Medical and Biological Engineering - School of Mechanical Engineering, University of Leeds LS2 9JT, England, UK

### ABSTRACT

Direct metallisation (DM) of tracks for electrical devices offers cost savings over traditional techniques due to the reduction of necessary lithographic manufacturing steps [1]. Additive manufacturing (AM), on its own, provides reduction in waste material and the formation of innovative 3-D shapes [2]. These two processes combined together for microelectronics manufacture, enable further economic benefits over existing manufacturing techniques. Polyetherimide (PEI) is a polymer used in AM whose properties enable its use for low error, high frequency electrical signal transmission and extreme environmental applications, such as space and aerospace [3]. Evidence has been provided in [4] for the enhanced optical sensitisation of PEI, enabling direct patterning for fast copper (Cu) track formation. Different PEI material shapes have been processed with enhanced sensitisation, to evaluate variability of the DM process for flexible and rigid electronics applications.

Highlighted in the figure are images of optically patterned PEI surfaces after electroless copper plating. The material surfaces include A) 3-D printed substrate, B) 1.75 mm diameter wire and C) 70  $\mu\text{m}$  thick flexible substrate. The minimum feature size obtained was influenced by the roughness of the substrate and its flatness. The 3-D printed substrate show metal deposits of thickness 0.5  $\mu\text{m}$ . The wire surface displayed a high uniformity enabling definition of 30  $\mu\text{m}$  wide Cu features, although the curved surface limited the area patterned. The flexible substrate provided the highest feature resolution with 10  $\mu\text{m}$  wide Cu features of thickness approximately 70 nm, as indicated in the track cross-section insert. After electroless Cu plating, the 3-D printed substrate and the wire showed conductivities approximately half of the value for bulk copper [5].

Direct metallisation of AM material PEI was successful using enhanced sensitisation chemistry, for a variety of material forms, where feature size and quality of tracks are limited by the substrate topography.



*Ultem 9085 Polyetherimide patterned with electroless copper for structures A) 3-D printed substrate, B) 1.75 mm diameter wire and C) 70  $\mu\text{m}$  thick flex with cross-section profil*

### References

1. D. E. Watson, et al. Silver nanocluster formation using UV radiation for direct metal patterning on polyimide. in 3rd Electronics System Integration Technology Conference ESTC. 2010. Berlin.
2. Thomas, D., Costs, benefits, and adoption of additive manufacturing: a supply chain perspective. The International Journal of Advanced Manufacturing Technology, 2016. 85(5): p. 1857-1876.
3. Fletcher, A.E., Advanced Organics for Electronic Substrates and Packages. 2013: Elsevier Science.
4. J. M. Hueso, et al., Fast photopatterning method enables selective plating for 2D and 3D micro-circuitry on polyetherimide. Advanced Functional Materials, DOI: 10.1002/adfm.201704451 2017.
5. Radoeva, M. and B. Radoev, Ohm resistivity of electroless copper layers as a function of their thicknesses. Journal of Materials Science, 1995. 30(9): p. 2215-2219.

### BIOGRAPHY

Dr Thomas Jones is a Research Associate at Heriot-Watt University, whose research interests include Direct Metallisation techniques on Additive Manufactured substrates.